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TFW 2616
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Branko D. KOVACEVIC
Title: SYSTEM FOR FRAME BASED AUDIO SYNCHRONIZATION AND METHOD THEREOF
Application No.: 09/833,493 Filed: April 11, 2001
Examiner: Daniel J. RYMAN Group Art Unit: 2616
Atty. Docket No.: ATL0100330 (1376-0100330)

MS AMENDMENT
COMMISSIONER FOR PATENTS
PO Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

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Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned brings the patents, publications, applications or other information identified in the attached:

- ☒ Form(s) PTO/SB/08A and/or PTO/SB/08B or PTO/1449
☒ Other: Copy of one cited non-patent reference

to the Examiner's attention in the above-identified application. Citation of such information shall not be construed as:

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For each item of information listed that is not in the English language, the undersigned has provided a concise explanation of the relevance, such as through (i) an English language abstract, (ii) an English language equivalent application, (iii) reference to discussion in the application, or (iv) if cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action that

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<u>Jennifer J. Jensen</u> Typed or Printed Name	<u>[Signature]</u> Signature

indicates the degree of relevance found by the foreign office.

STATEMENT UNDER 37 C.F.R. § 1.704(d)

If the above-identified application is an original application filed on or after May 29, 2000:

- ☐ each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application and this communication was not received by any individual designated in § 1.56(c) more than thirty days prior to the filing of this Information Disclosure Statement.

FEES DUE

This Information Disclosure Statement is being filed:

- ☐ within three months of the filing date of a national application or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.
- ☐ before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.
- ☐ during the period specified in § 1.97(c). Each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application not more than three months prior to the filing of this Information Disclosure Statement.
- ☐ during the period specified in § 1.97(c). Accordingly, the fee set forth in § 1.17(p) is required and provided as shown on the attached Fee Transmittal.
- ☐ during the period specified in § 1.97(d). Accordingly, the fee set forth in § 1.17(p) is required and provided as shown on the attached Fee Transmittal. Additionally, each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application not more than three months prior to the filing of this Information Disclosure Statement.

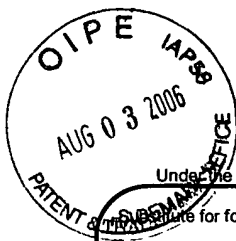
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31 July 2066
Date

Respectfully submitted,



Ryan S. Davidson, Reg. No. 51,596
LARSON NEWMAN ABEL POLANSKY & WHITE, LLP
5914 West Courtyard Dr., Suite 200
Austin, Texas 78730
(512) 439-7100 (phone)
(512) 439-7199 (fax)



PTO/SB/08B (07-05)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	09/833,493
Filing Date	April 11, 2001
First Named Inventor	Branko D. KOVACEVIC
Art Unit	2616
Examiner Name	Daniel J. RYMAN
Attorney Docket Number	ATI.0100330 (1376-0100330)

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	A1	SONDERMEYER, Jeff; "Rational Sample Rate Conversion with Blackfin Processors"; Engineer to Engineer Note EE-183; Analog Devices; January 20, 2003; Pages 1-25	
	B1		
	C1		
	D1		
	E1		
	F1		
	G1		
	H1		
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